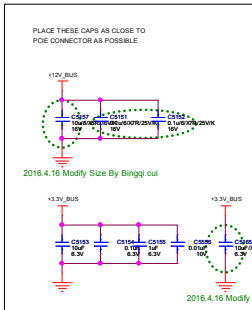
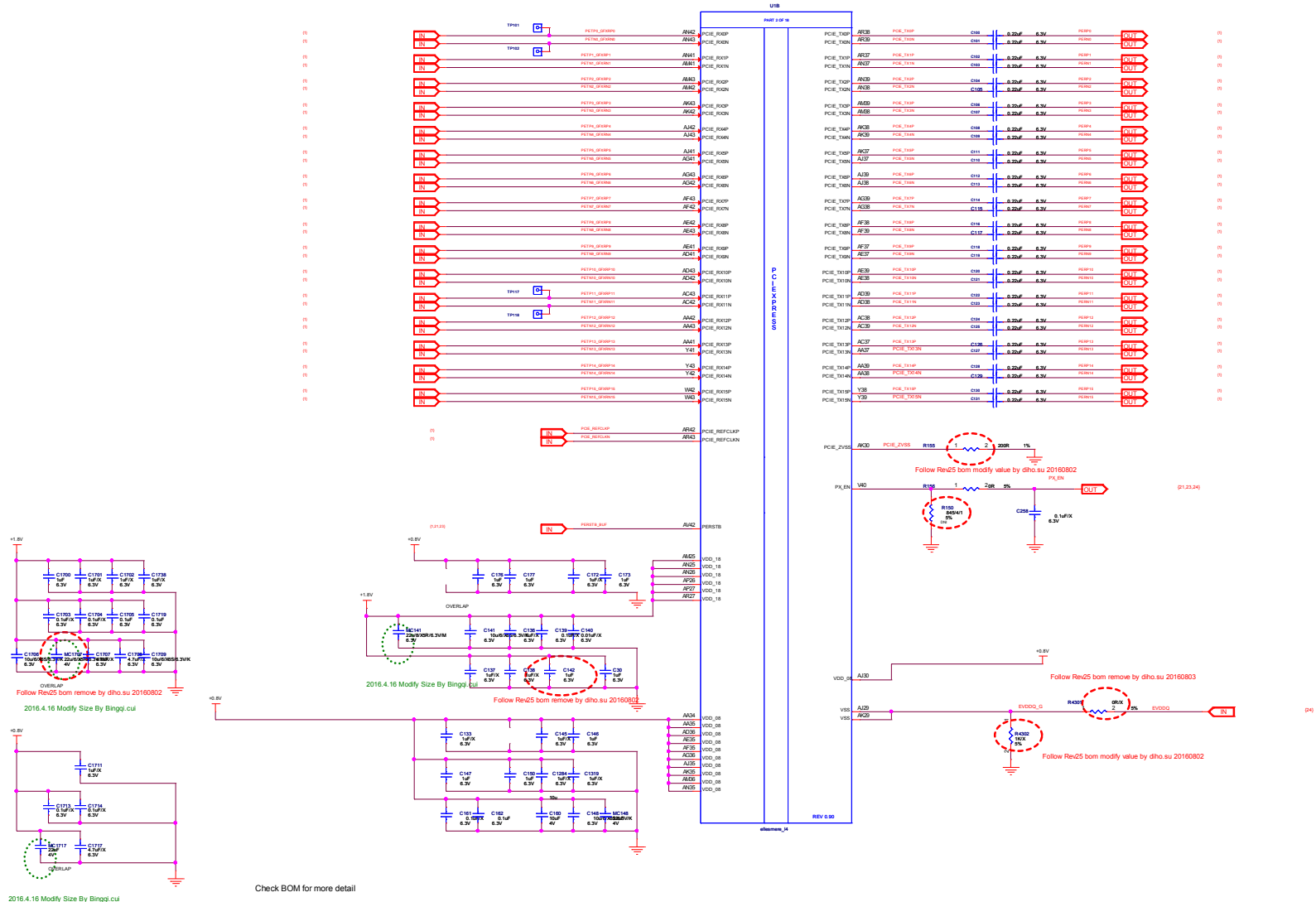


(1) PCI-EXPRESS EDGE CONNECTOR

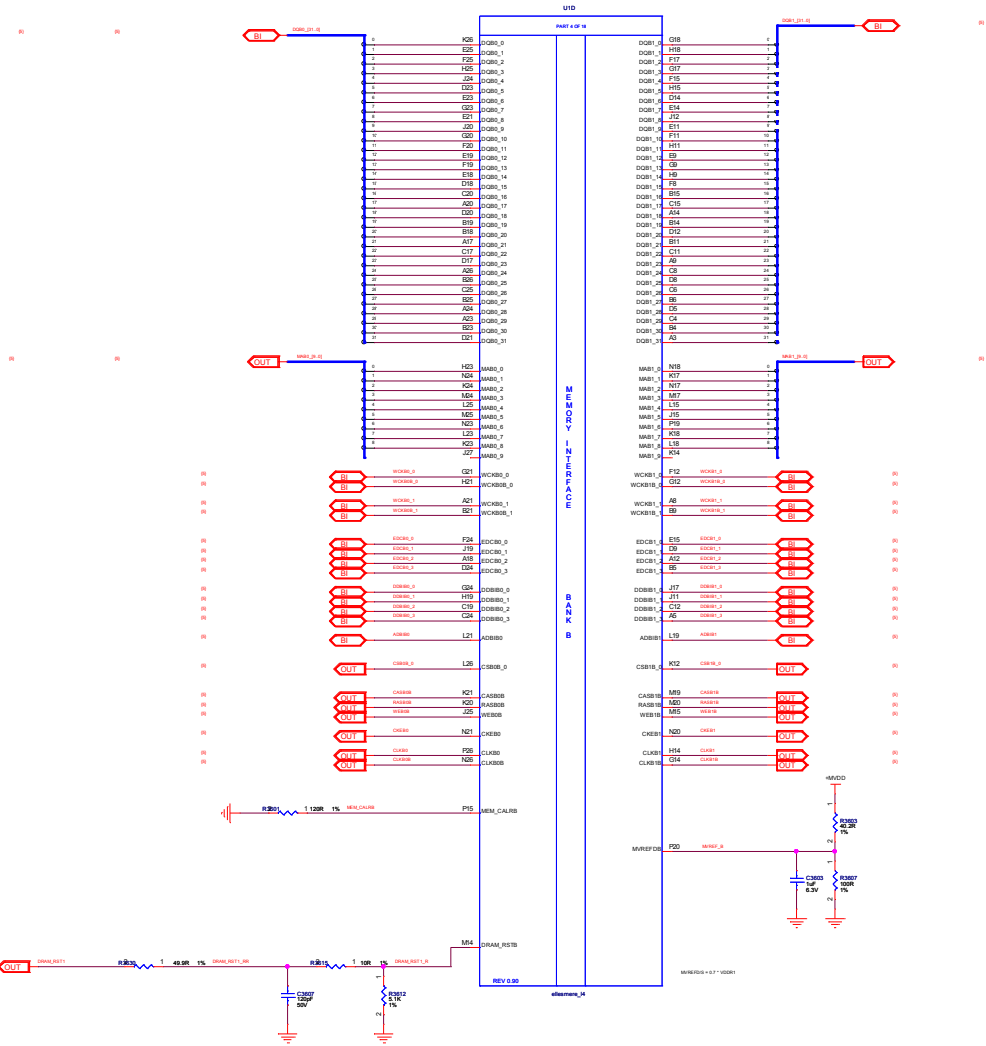
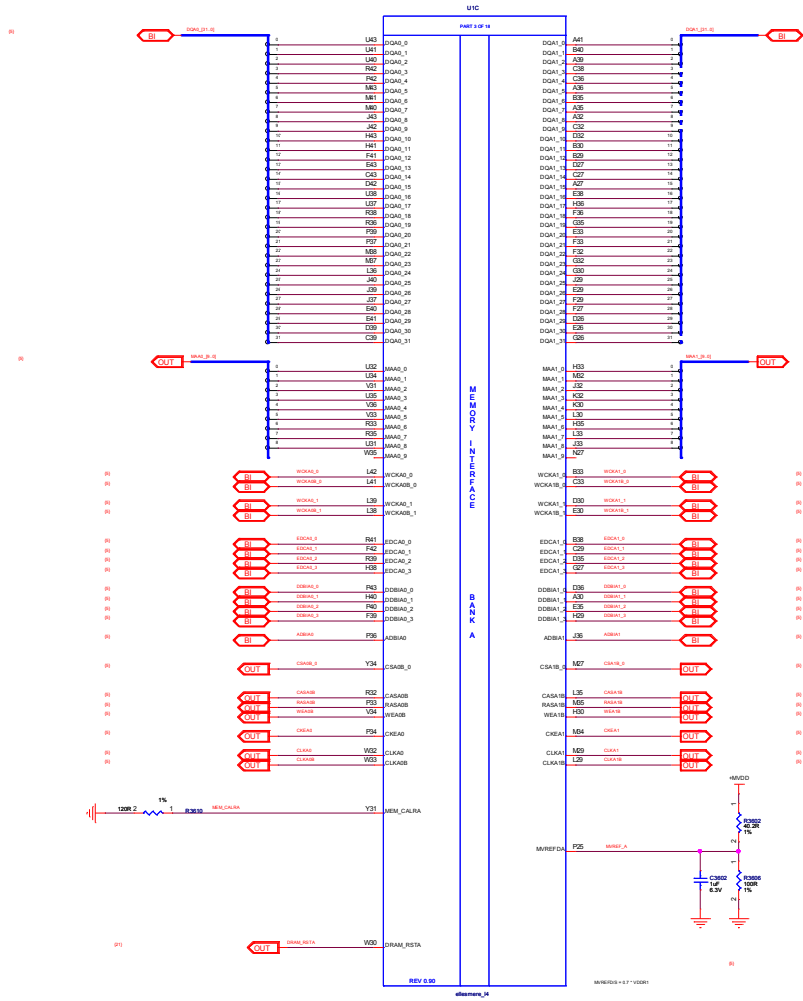


GIGABYTE			
Title PCI Express			
Size	Custom	Document Number	Rev
		GV-RX570GAMING-4GD	1.0
Date:	Wednesday, March 01, 2017		Sheet 1 of 29

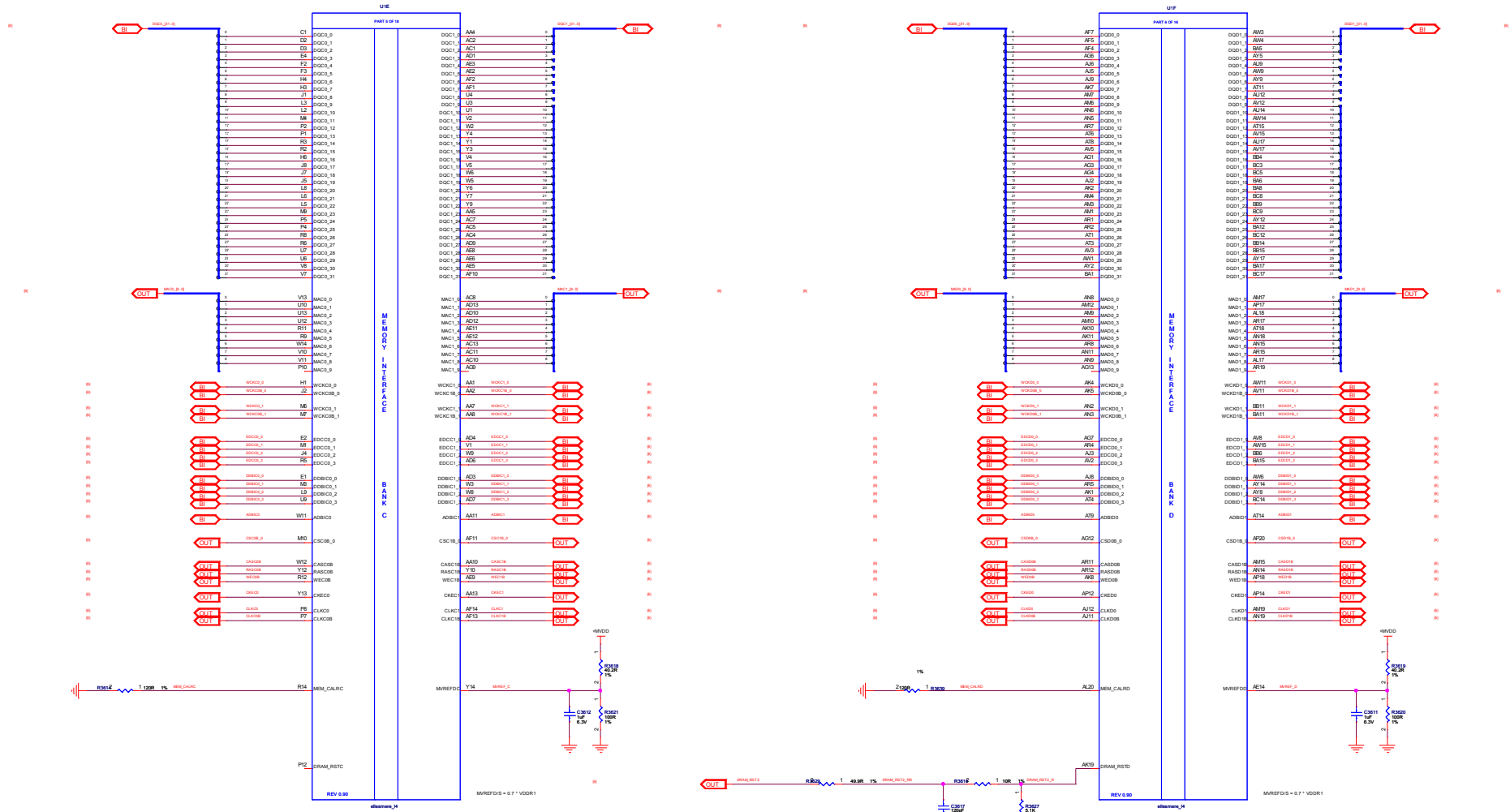
(2) ELLESMERE PCIE INTERFACE



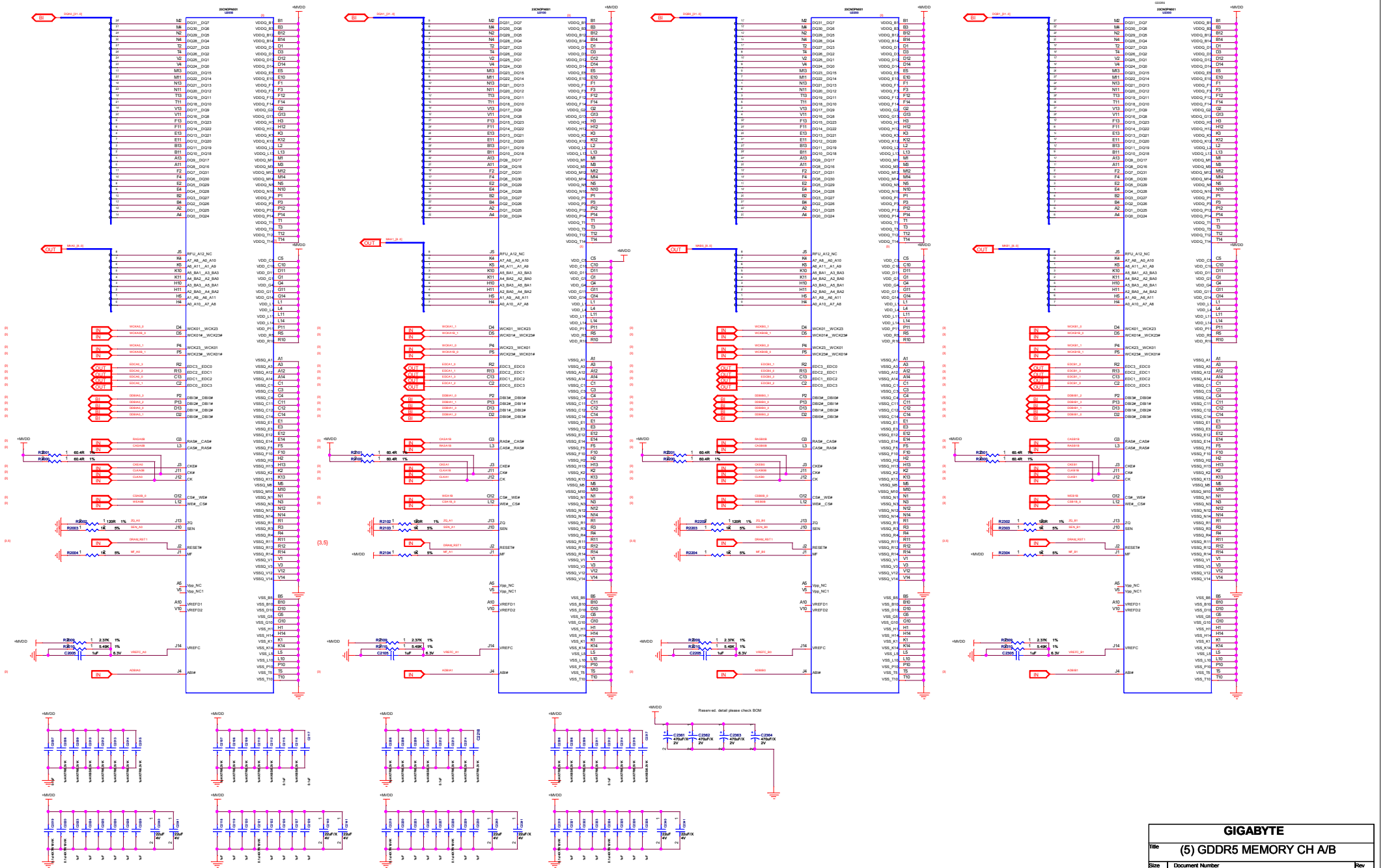
(3) ELLESMERE MEM INTERFACE CH A/B



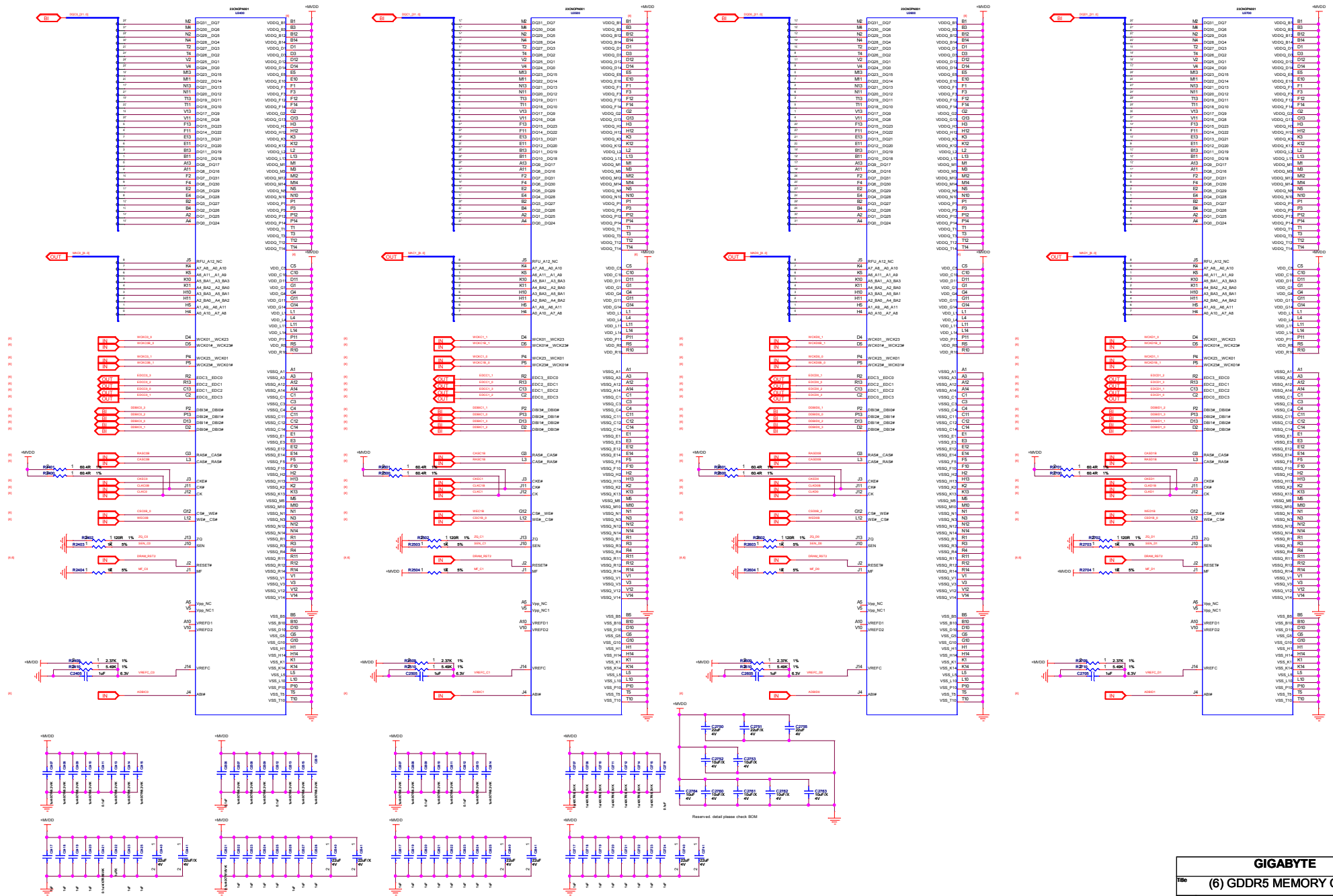
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B

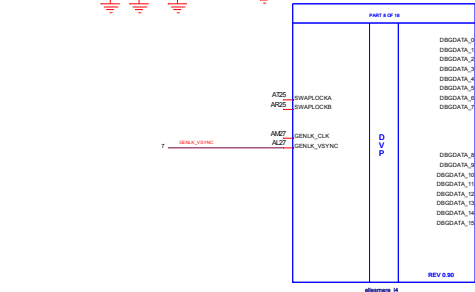
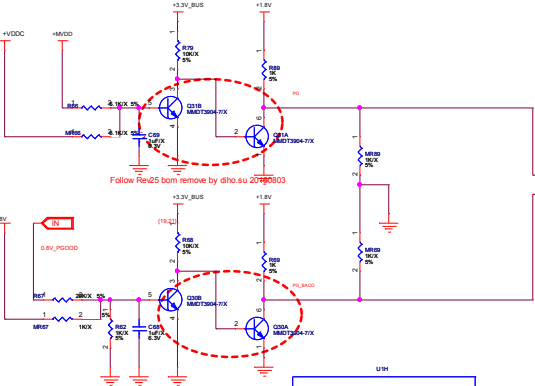


(6) GDDR5 MEMORY CH C/D



(7) ELLESMERE GPIO STRAP CF XTAL

SCUADA BUS		
IO ADDRESS	FUNCTION	DEVICE
DDCVGA BUS		
IO ADDRESS	FUNCTION	DEVICE
0x00	EXT TEMP SENSOR	LM8903



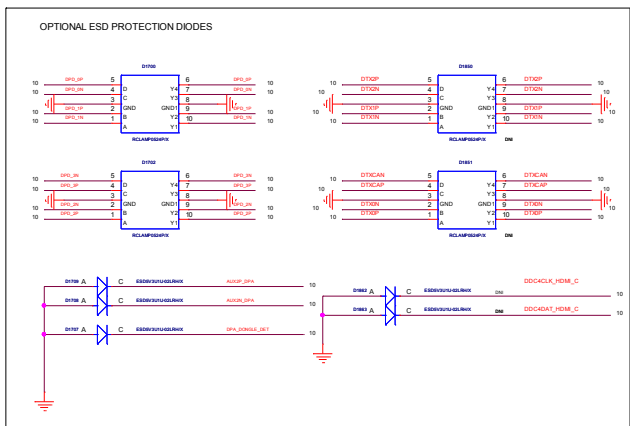
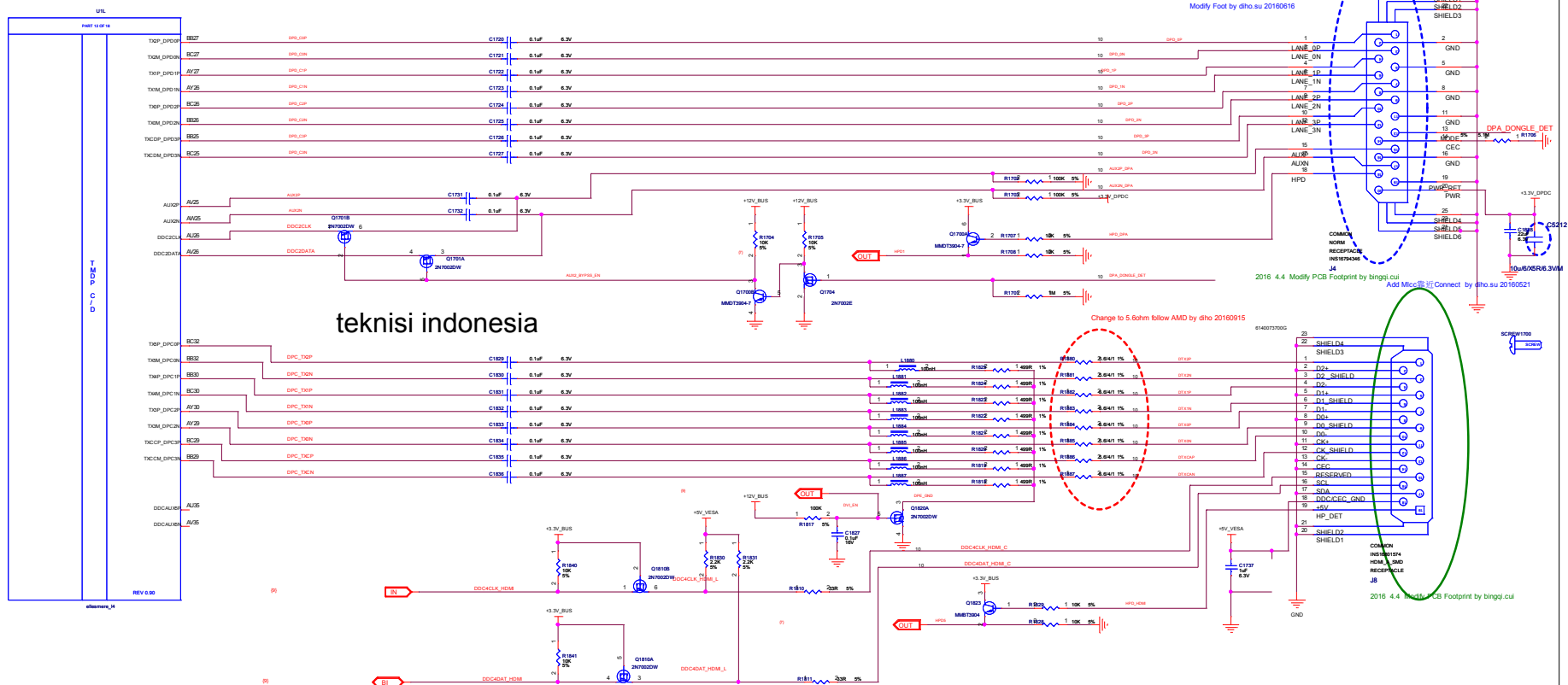
(8) ELLESMERE DAC1 LOCK

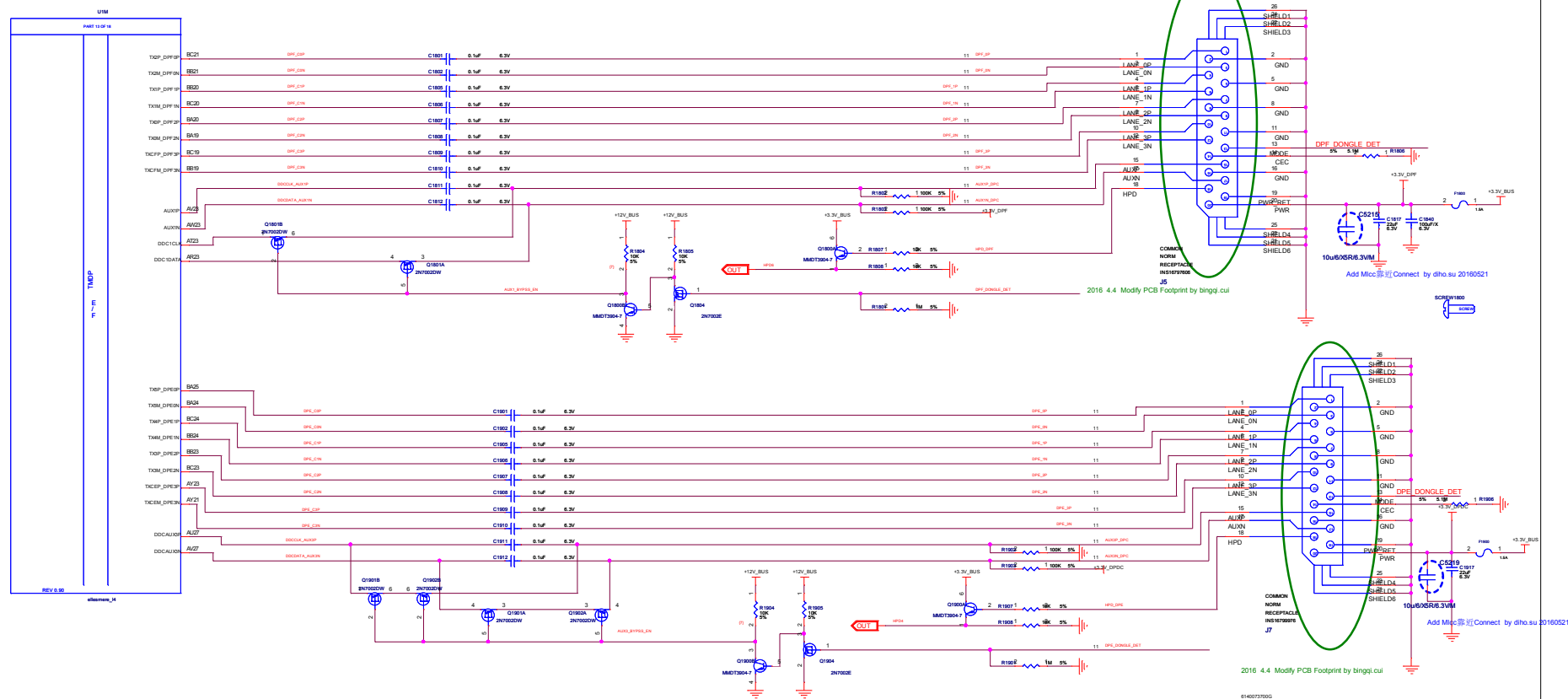


GIGABYTE			
Title ELLESMERE DAC1 LOCK			
Size	Document Number		Rev
Custom	GV-RX570GAMING-4GD		1.0
Date:	Wednesday, 2017	Sheet	8 of 29

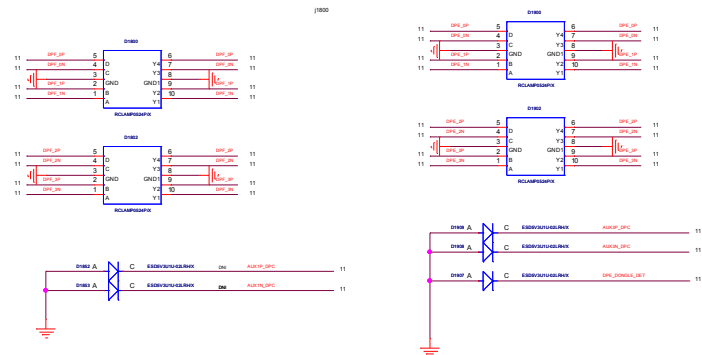


teknisi indonesia

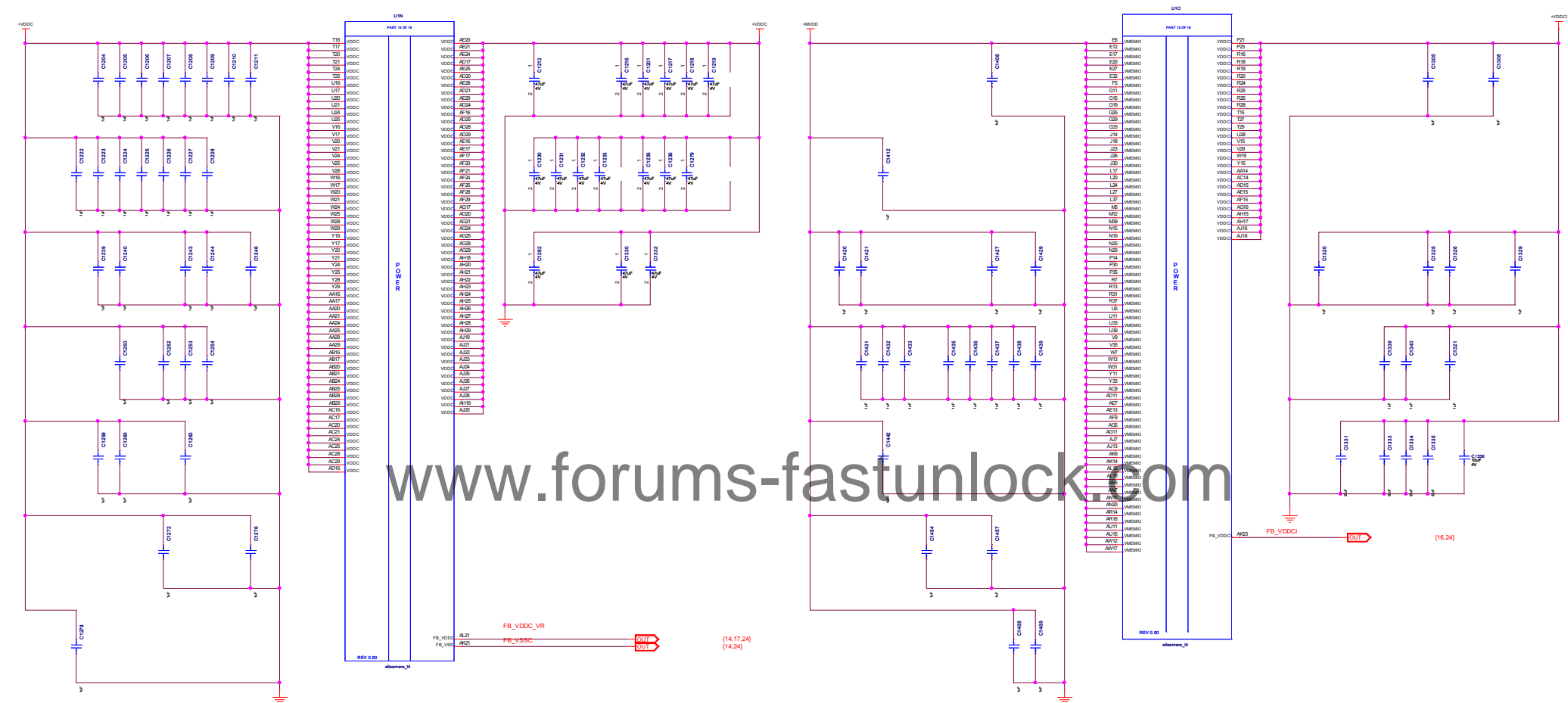




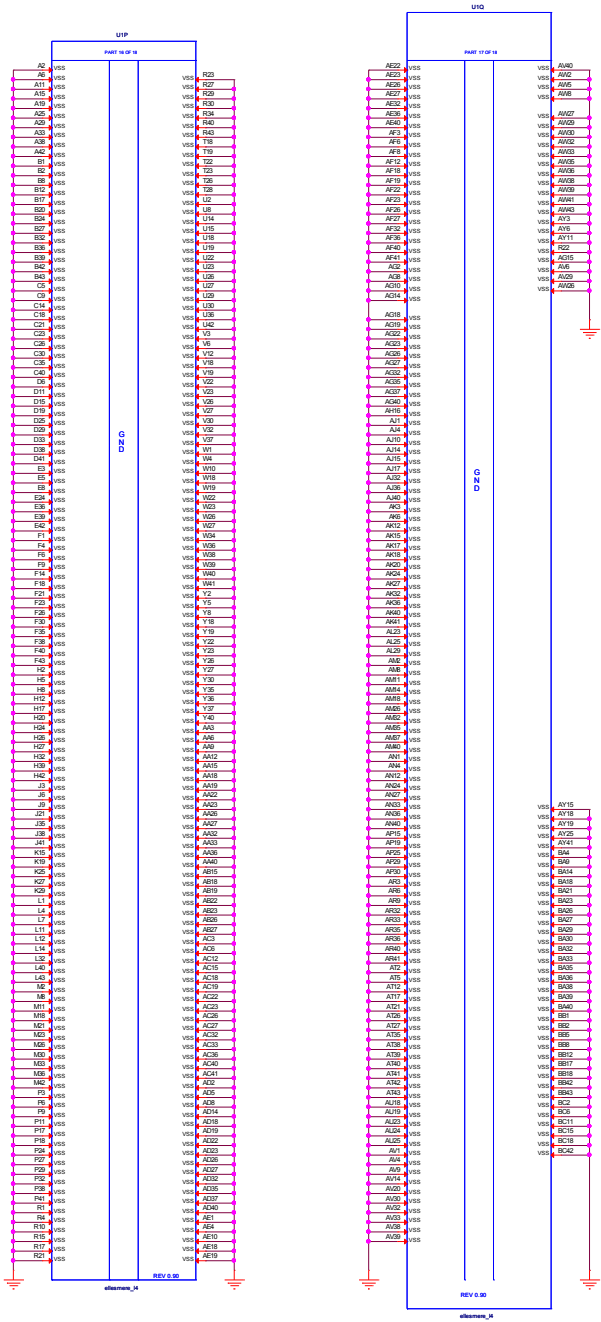
OPTIONAL ESD PROTECTION DIODES



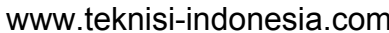
GIGABYTE			
Title ELLESMERE LVTMDP E/F			
Size	Document Number		Rev
Custom	GV-R570GAMING-4GD		1.0
Date:	Wednesday, March 01, 2017	Sheet	11 of 29

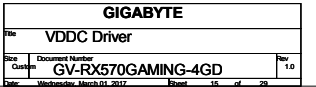


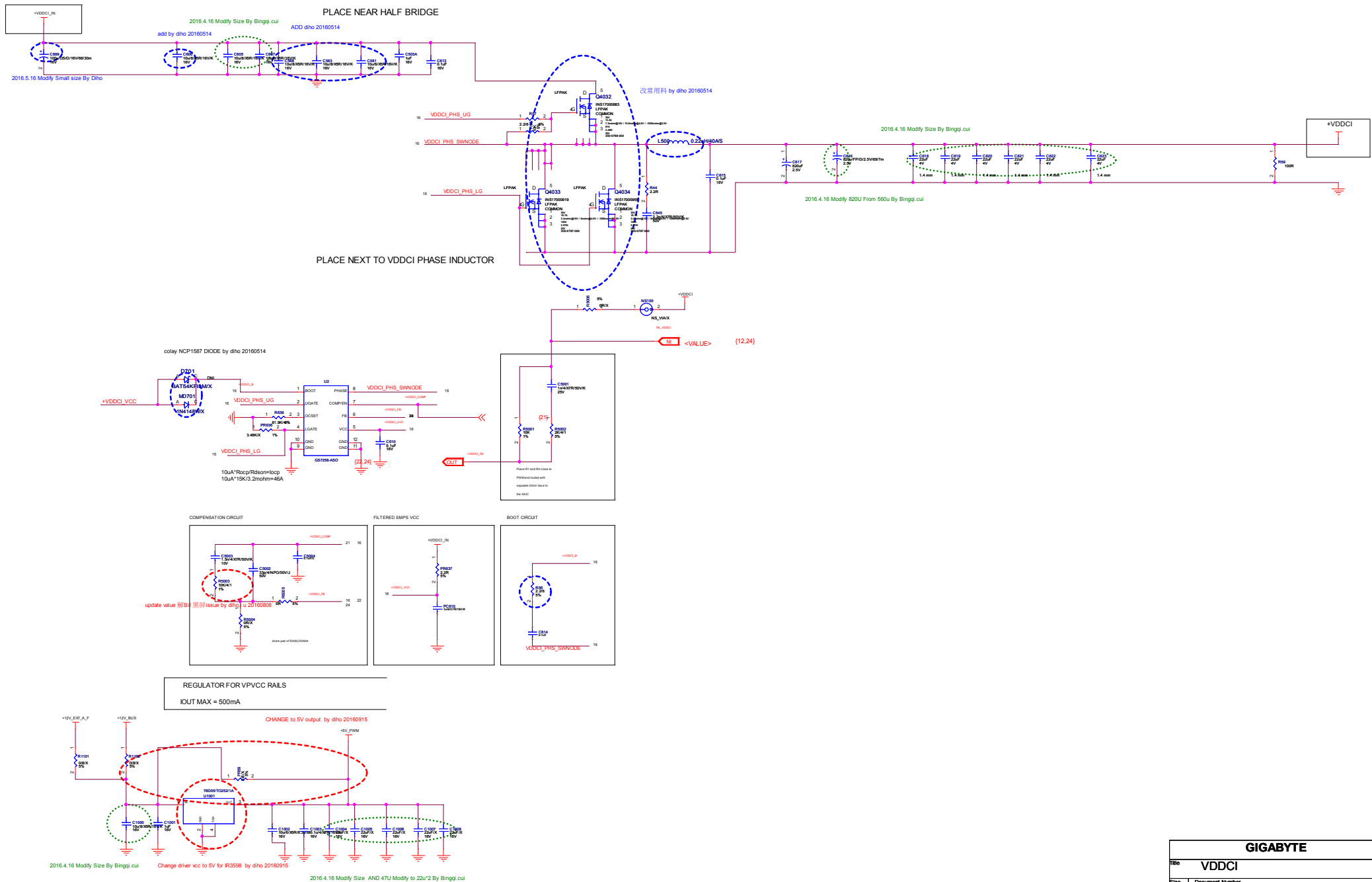
(13) ELLESMERE GROUND



GIGABYTE			
Title ELLESMERE GND			
Size	Document Number		Rev
Customer	GV-RX570GAMING-4GD		1.0
Date	Wednesday, March 01, 2017	Sheet	13 of 29



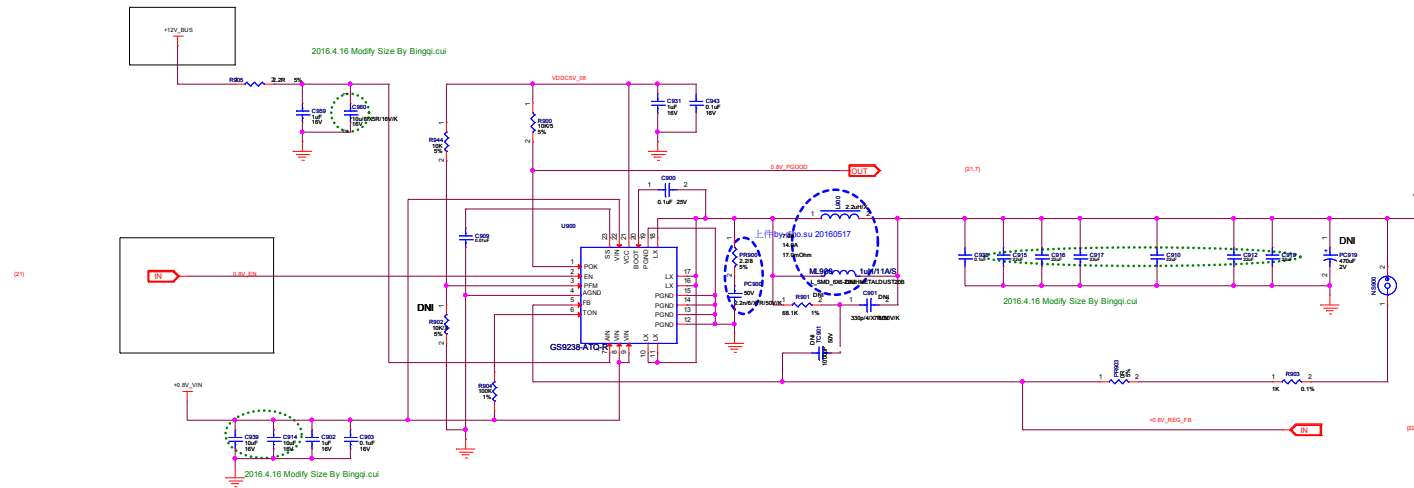




GIGABYTE			
File	VDDCI		
Size	Document Number	Rev	
Cuskin	GV-RX570GAMING-4GD	1.0	
Date	Wednesday, March 01, 2017	Sheet	16 of 28



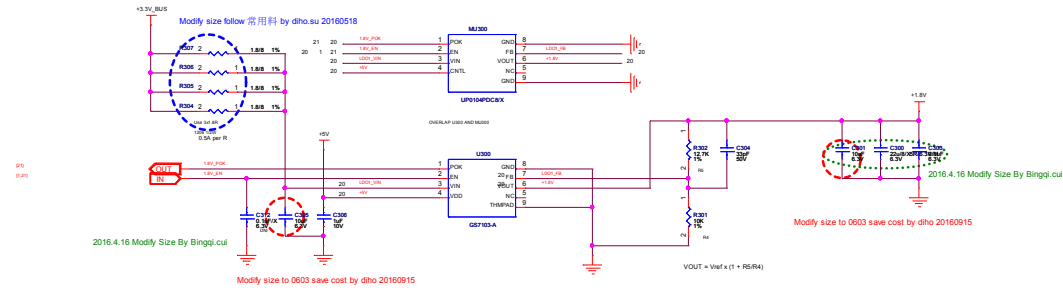
(17) 0.95V



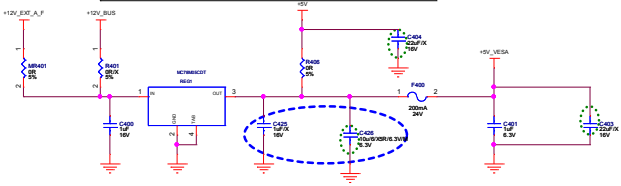
GIGABYTE			
Title 0.95V			
Size Custom	Document Number GV-RX570GAMING-4GD		Rev 1.0
Date:	Wednesday, March 01, 2017	Sheet	19 of 29

(18) SMALL RAIL REGULATORS

LDO #1: VIN = 3.0V TO 3.6V MAX
VOUT = +1.8V +/- 2%
IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

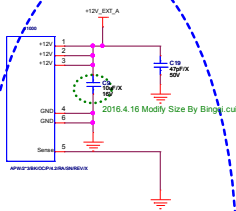


REGULATOR FOR +5V RAILS
IOUT MAX = 150mA

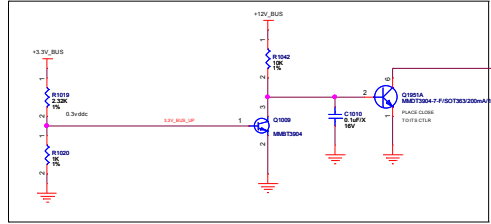
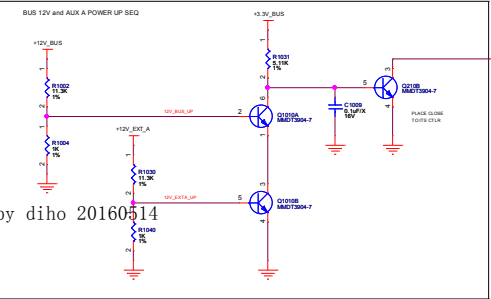


GIGABYTE			
Title SMALL RAIL REGULATORS			
Size	Document Number		Rev
Custom	GV-RX570GAMING-4GD		1.0
Date: Wednesday, March 01, 2017		Sheet 20 of 29	

(19) POWER MANAGEMENT



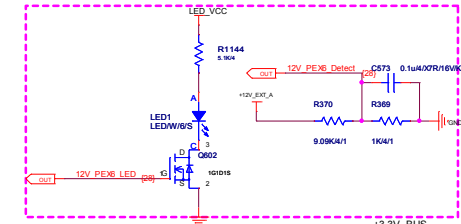
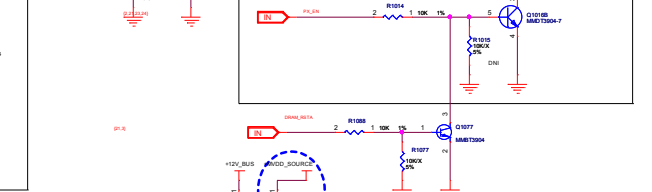
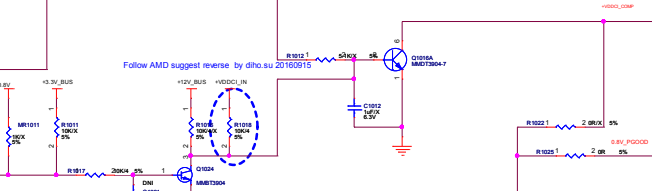
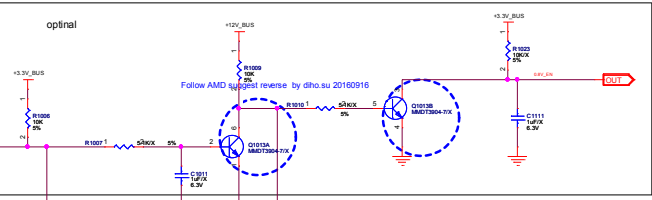
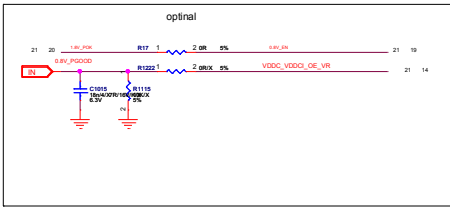
6PIN&8PIN CONNECT COLAY by diho 20160514



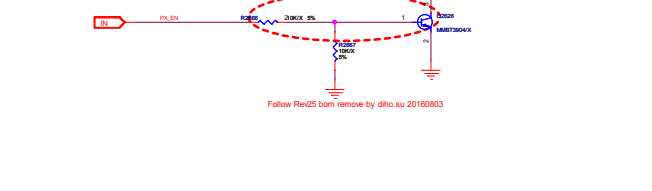
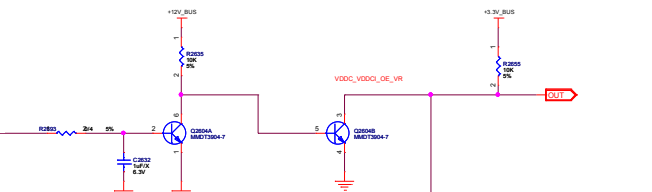
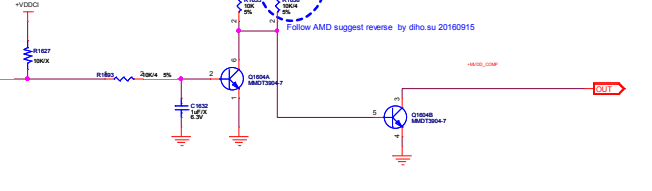
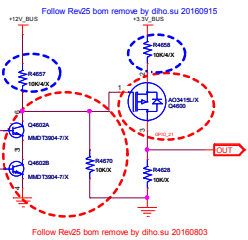
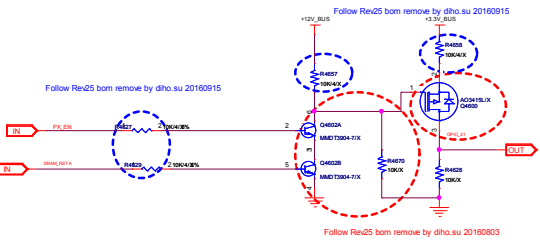
POWER UP SEQUENCE

12V_BUS	12V_EXT_A	12V_2/3/4
12V_BUS	12V_EXT_A	12V_2/3/4
12V_BUS	12V_EXT_A	12V_2/3/4

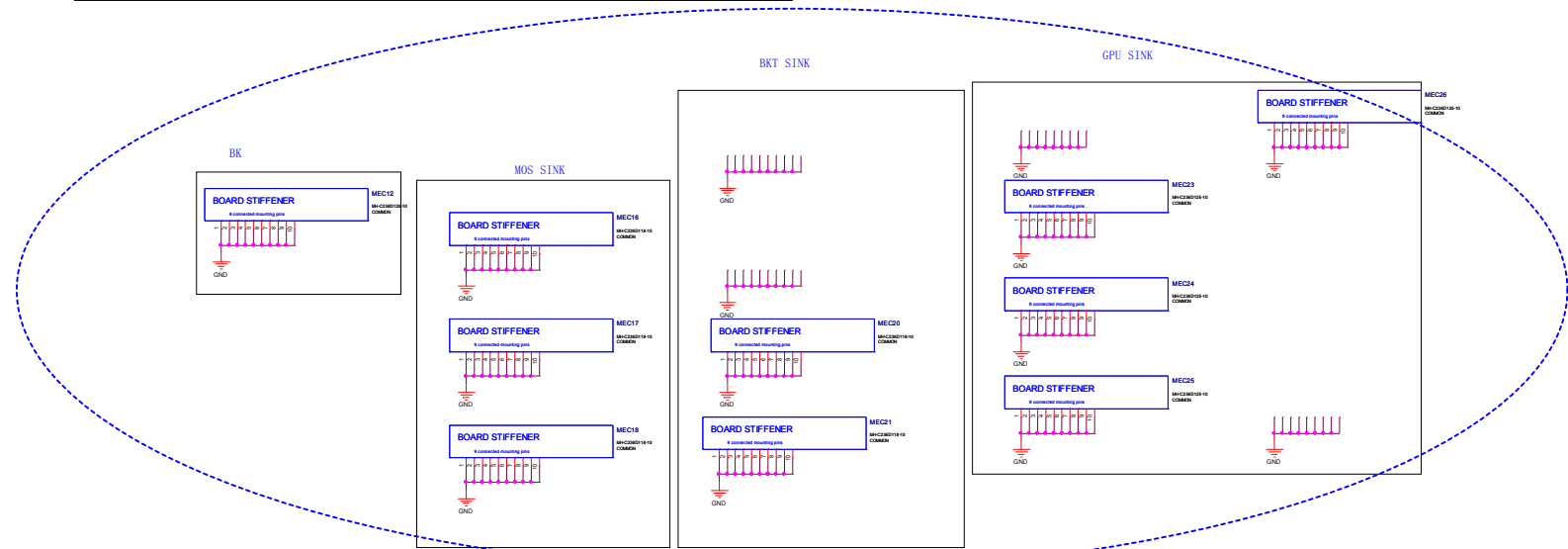
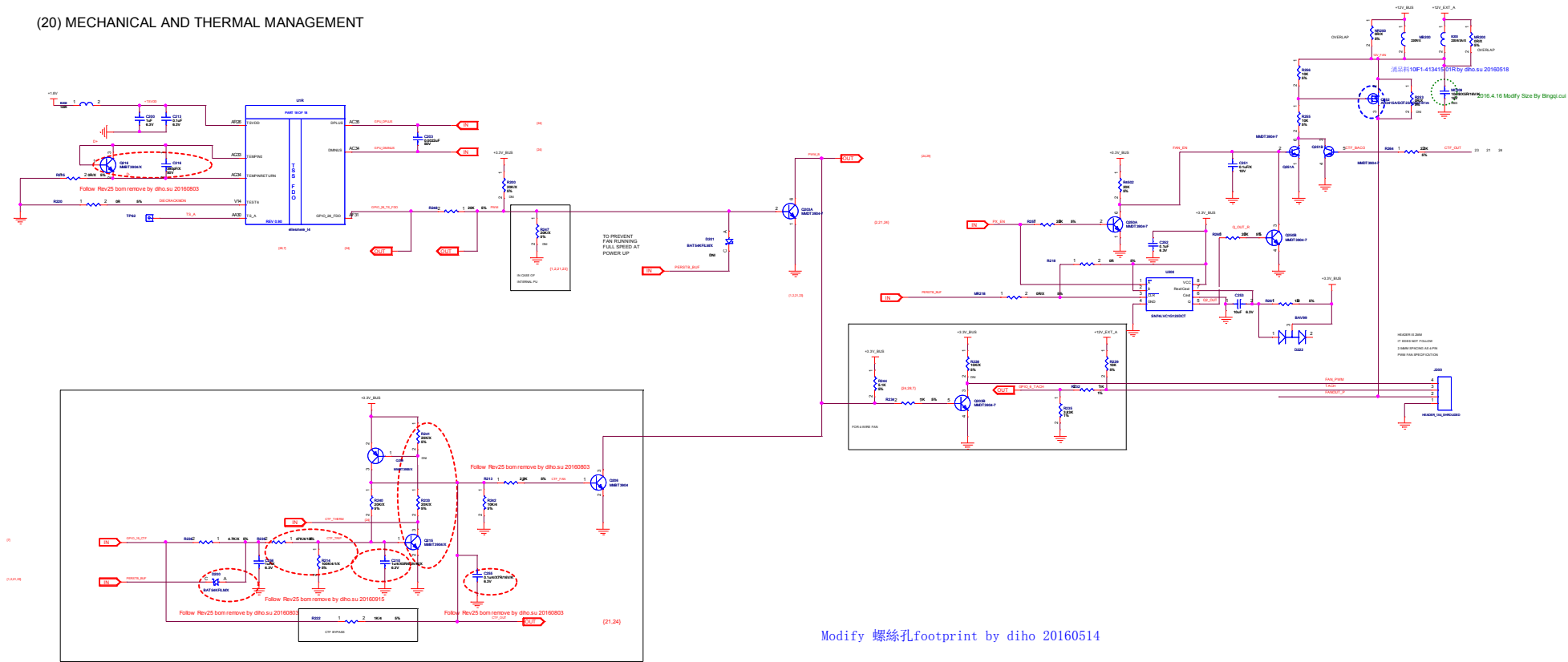
teknisi indonesia



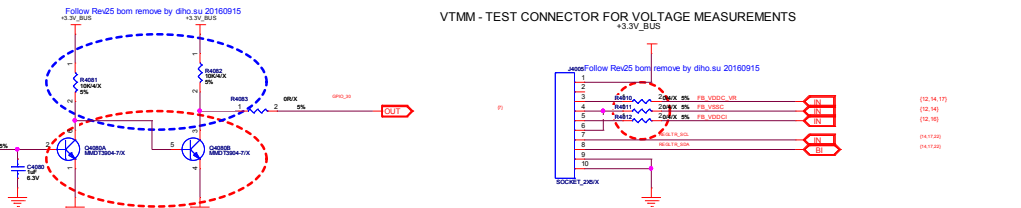
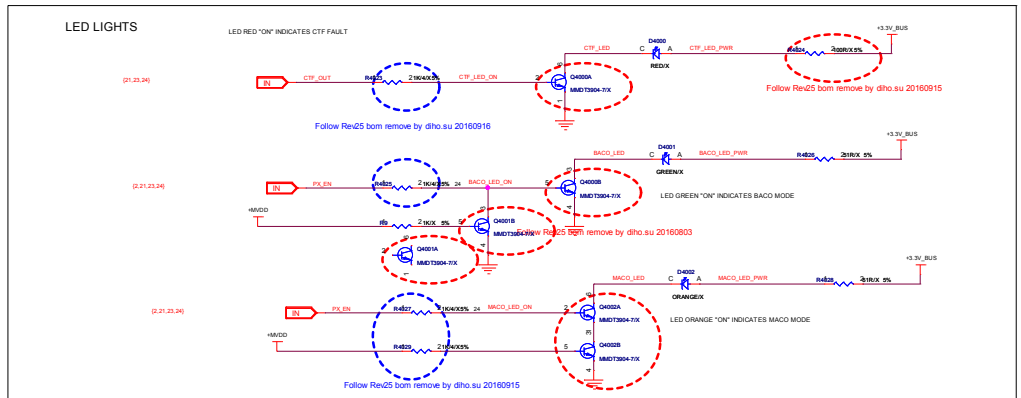
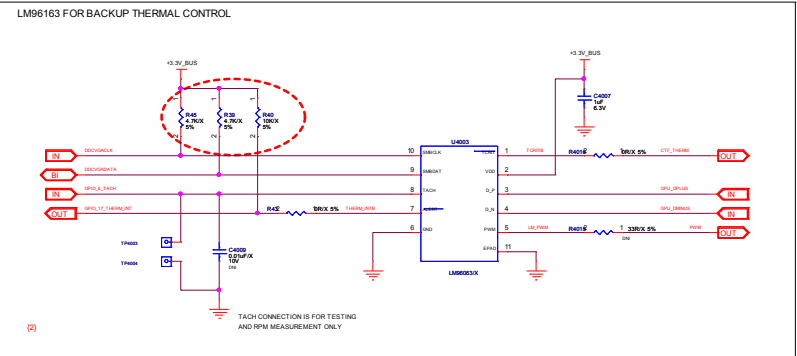
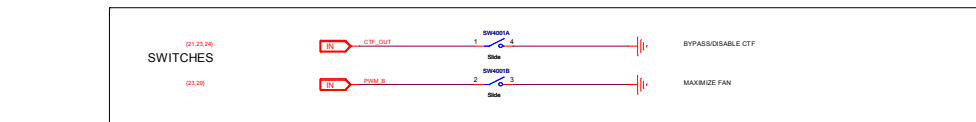
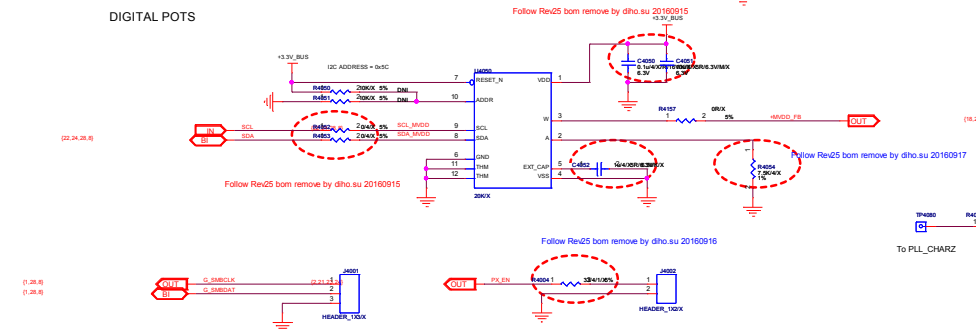
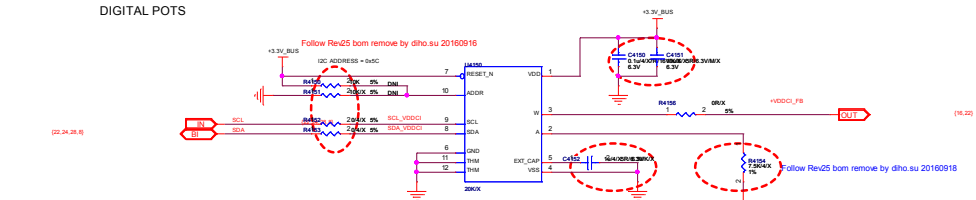
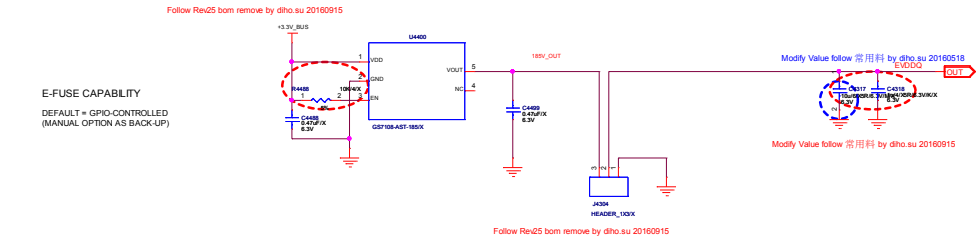
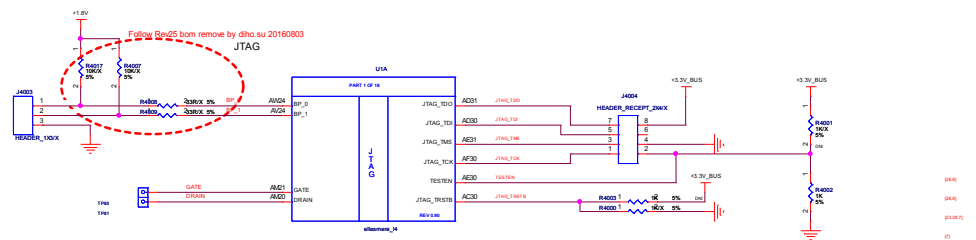
teknisi indonesia



(20) MECHANICAL AND THERMAL MANAGEMENT

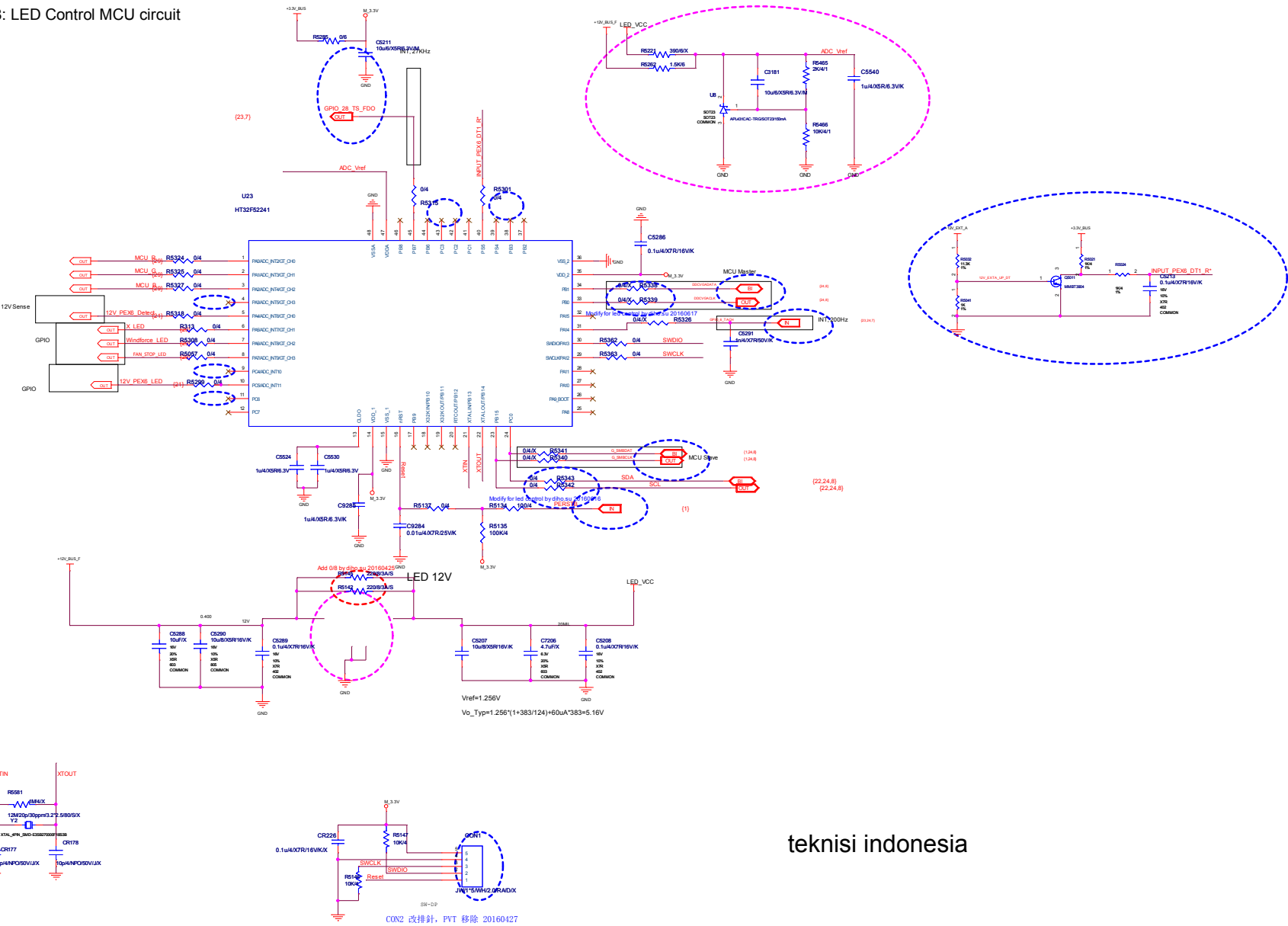


(21) DEBUG CIRCUITS



0	00A	00000000	
1	00B	00000000	1. Add SPIN1 to registers 13000 2. update 13000 when response
2	00C	00000000	PCSR: - add enable register 013000 - 013007 - add pull-down indicator 13000 - 13007 Remove C400, C403, V0400, C410, C41A, R400
3	00D	00000000	Modify "G07 Design"





tekni indonesia

Vout = 8 ~ 7.5V

ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS." THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

ASSEMBLY
PAGE DETAIL

ASSEMBLY DESCRIPTION
MECH Bracket Thermal

File		
Size	Document Number	Rev
Customer	04-43070GAMING-4GD	1.0
Date	Wednesday, March 01, 2017	Sheet 28 of 29

Add MOS VRHOT CIRCUIT

